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APPLICANT NAME	1 . Dr.Murali Malijeddi 2 . Dr.K.B.S.D Sarma 3 . Dr.S.Sambasiva Rao Dannina 4 . Dr.Kommalapati Rajesh 5 . Mr.N.Mohankumar 6 . Mr.Srikanth Parikibandla 7 . Dr.Arunkumar Madupu 8 . Mr.Yogendra Kumar 9 . Ms.Minakshi Sanadhya 10 . Mr.Shaik Karimullah
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E-MAIL (As Per Record)	tumula.githam@gmail.com
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(71)Name of Applicant :

1)Dr.Murall Maljreddi
Address of Applicant :Professor, Dean R&D, Department of Electronics and Communications, School of Engineering, Centurion University of Technology and Management, Andhra Pradesh, India. Pin Code:535003 Vizianagaram -----

2)Dr.K.B.S.D Sarma
3)Dr.S.Sambasiva Rao Dannina
4)Dr.Kommalapati Rajesh
5)Mr.N.Mohankumar
6)Mr.Srikanth Parikibandla
7)Dr.Arunkumar Madupu
8)Mr.Yogendra Kumar
9)Ms.Minakshi Sanadhya
10)Mr.Shaik Karimullah

Name of Applicant : NA
Address of Applicant : NA

(72)Name of Inventor :
1)Dr.Murall Maljreddi
Address of Applicant :Professor, Dean R&D, Department of Electronics and Communications, School of Engineering, Centurion University of Technology and Management, Andhra Pradesh, India. Pin Code:535003 Vizianagaram -----

2)Dr.K.B.S.D Sarma
Address of Applicant :Professor, Department of ECE, BVC College of Engineering (Autonomous), Odalarevu, East Godavari District, Andhra Pradesh, India. Pin Code:533210 East Godavari -----

3)Dr.S.Sambasiva Rao Dannina
Address of Applicant :Associate Professor, Department of ECE, Prasiddha College of Engineering & Technology, Amalapuram, Andhra Pradesh, India. Pin Code:533222 East Godavari -----

4)Dr.Kommalapati Rajesh
Address of Applicant :Assistant Professor, Department of ECE, Audisankara Institute of Technology, Gudur, Nellore District, Andhra Pradesh, India. Pin Code:524101 Nellore -----

5)Mr.N.Mohankumar
Address of Applicant :Assistant Professor, Department of Electronics and Communication, Engineering, Amrita School of Engineering, Amrita Vishwa Vidyapeetham, Coimbatore, Tamil Nadu, India. Pin Code:641112 Coimbatore -----

6)Mr.Srikanth Parikibandla
Address of Applicant :Assistant Professor, School of Engineering, Malla Reddy University, Maisammaguda, Hyderabad, Telangana, India. Pin Code:500100 Hyderabad -----

7)Dr.Arunkumar Madupu
Address of Applicant :Associate Professor, Department of ECE, Malla Reddy College of Engineering and Technology, Hyderabad, Telangana, India. Pin Code: 500100 Hyderabad -----

8)Mr.Yogendra Kumar
Address of Applicant :Assistant Professor, Department of Physics, VSP Government (PG) College, Kairana, Shamli, Uttar Pradesh, India. Pin Code:247774 Shamli -----

9)Ms.Minakshi Sanadhya
Address of Applicant :Assistant Professor, Department of Electronics and Communication Engineering, SRMIST, NCR Campus, Modinagar, Ghaziabad, Uttar Pradesh, India. Pin Code:201204 Modinagar -----

10)Mr.Shaik Karimullah
Address of Applicant :Assistant Professor, Department of ECE, Annamacharya Institute of Technology and Sciences, Rajampet, Andhra Pradesh, India. Pin Code:516126 Kadapa -----

(57) Abstract :

The present invention discloses a machine learning based system for Physical Attack Protection for VLSI Chip Level Hardware Security and method thereof. The system includes, but not limited to, a plurality of crypto circuits configured with a plurality of peripheral circuits having plain and cipher texts communicatively coupled through a machine learning based digital data interface (I/F). Further, the core power supply (core VDD) voltage is configured to regulate with respect to local ground voltage (VSS) by power management circuits (PMCs), which is connected with DC-DC converters and reference voltage generators. Accompanied Drawing [FIG. 1]

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